

FIGURE 1A

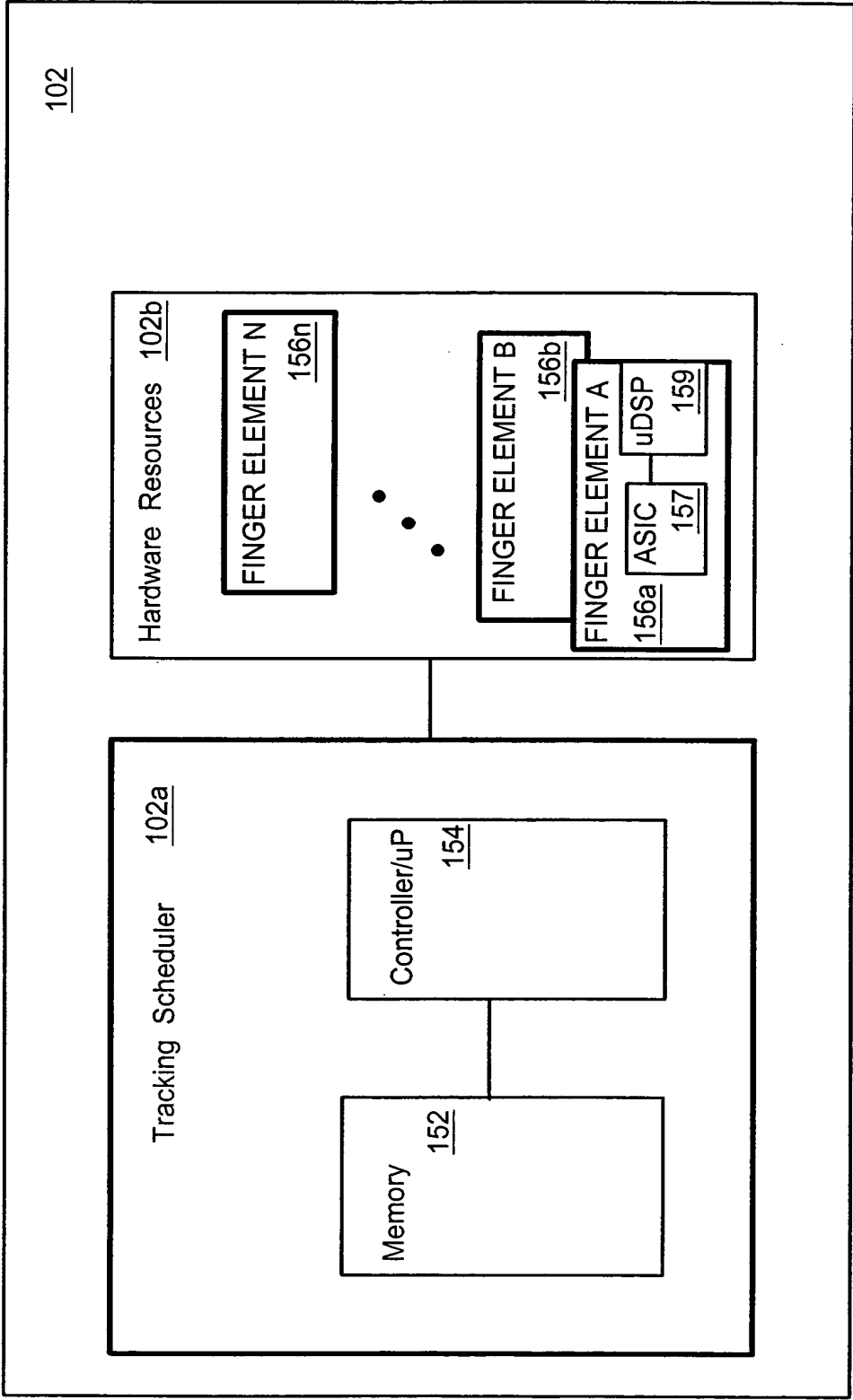


FIGURE 1B

160

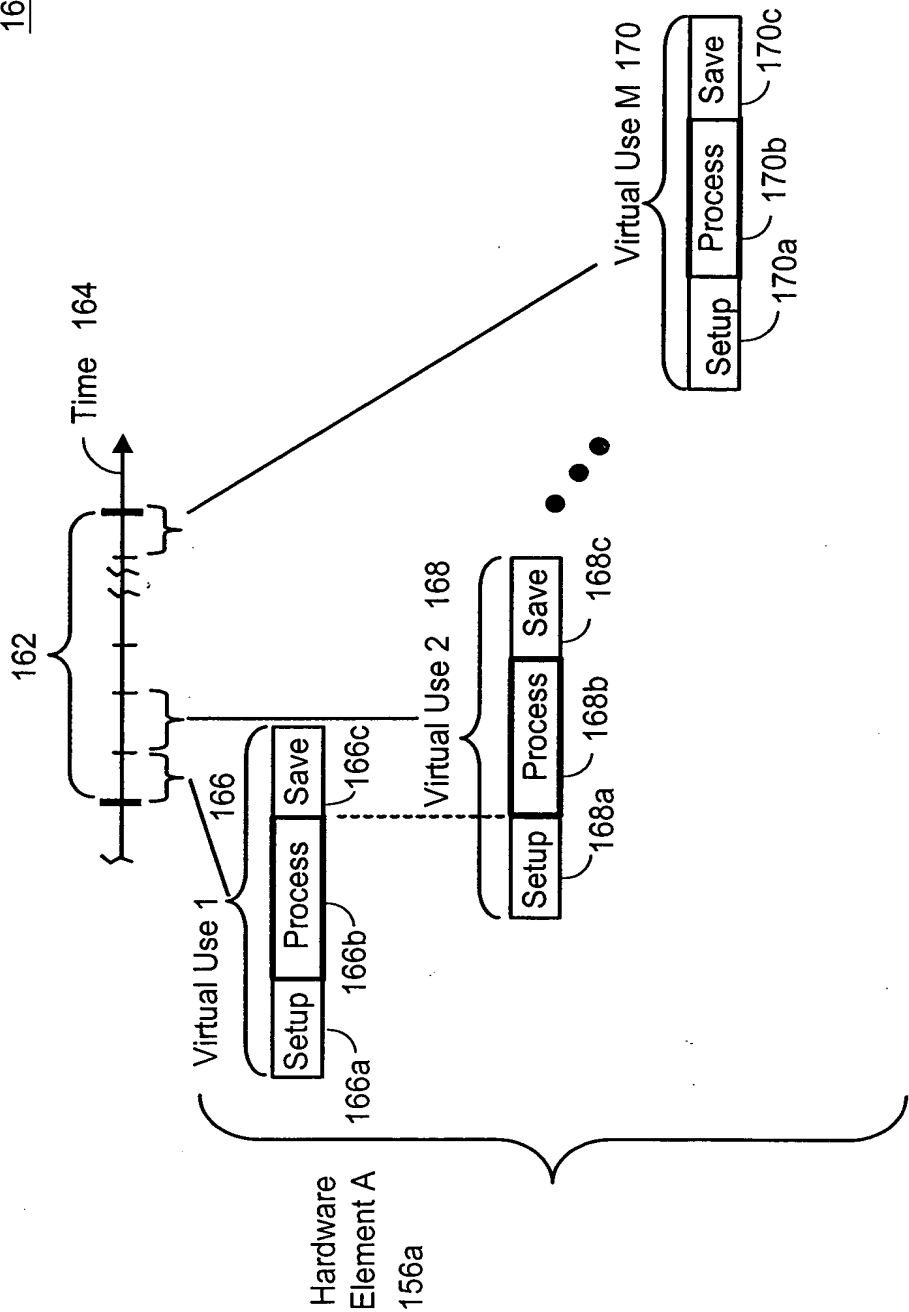


FIGURE 1C

200A

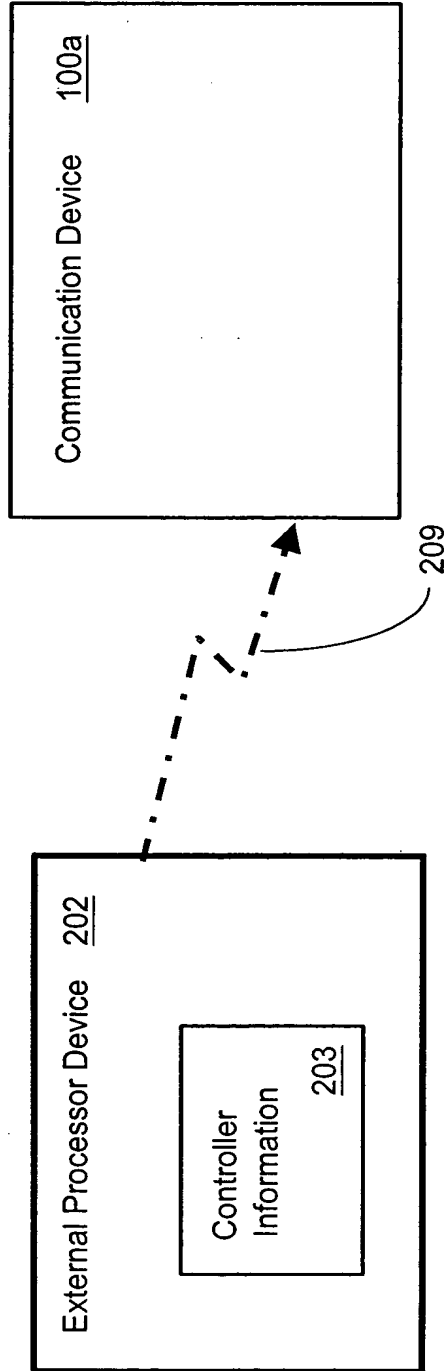


FIGURE 2A

202

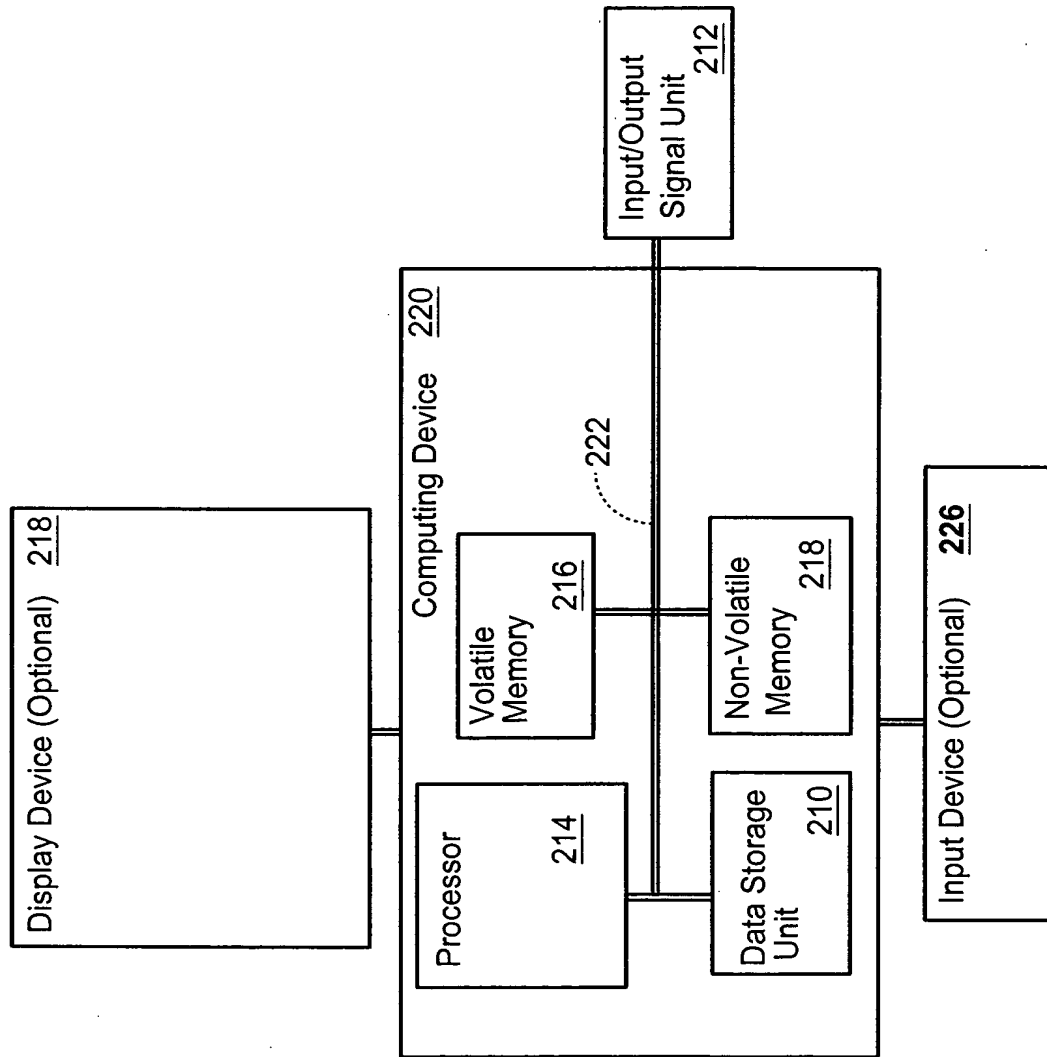


FIGURE 2B

200C

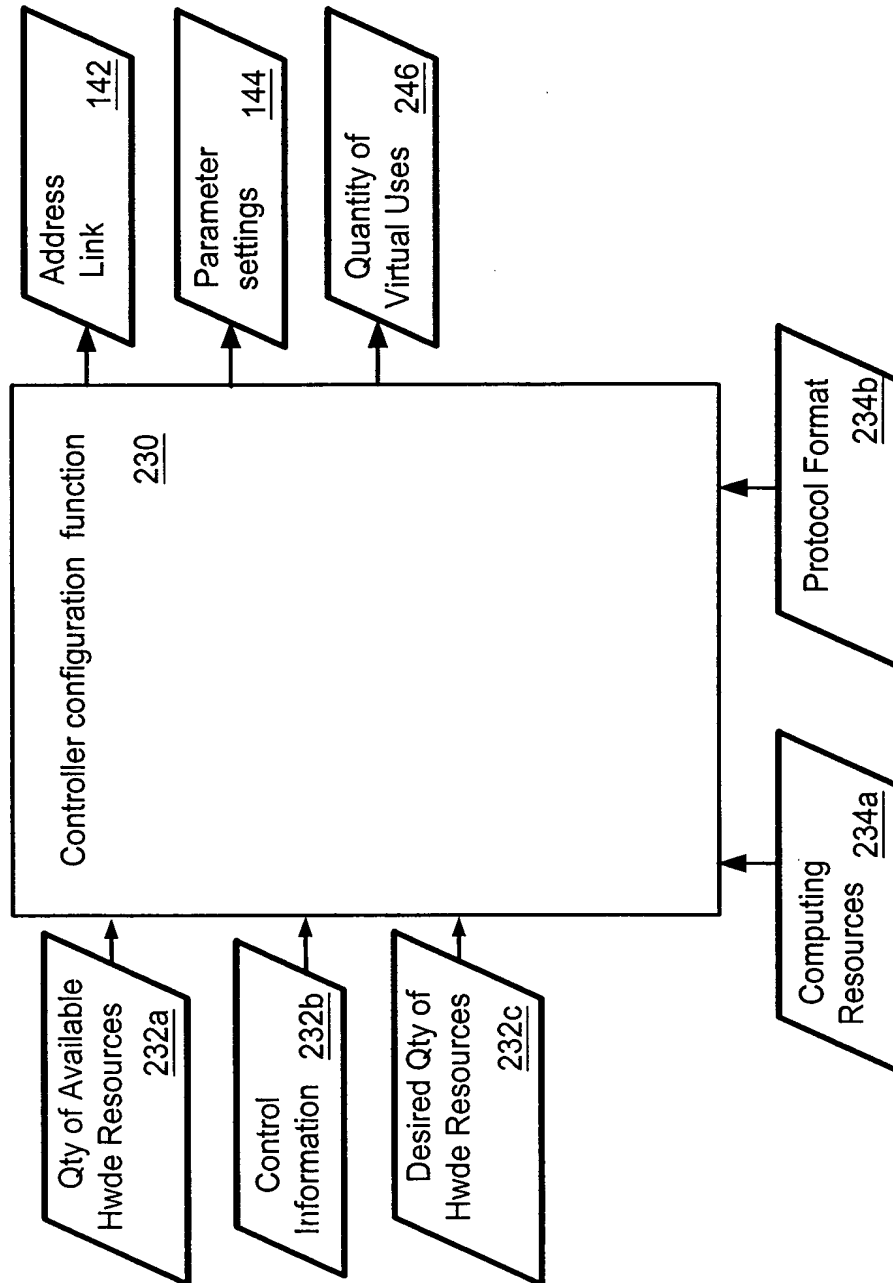


FIGURE 2C

300A

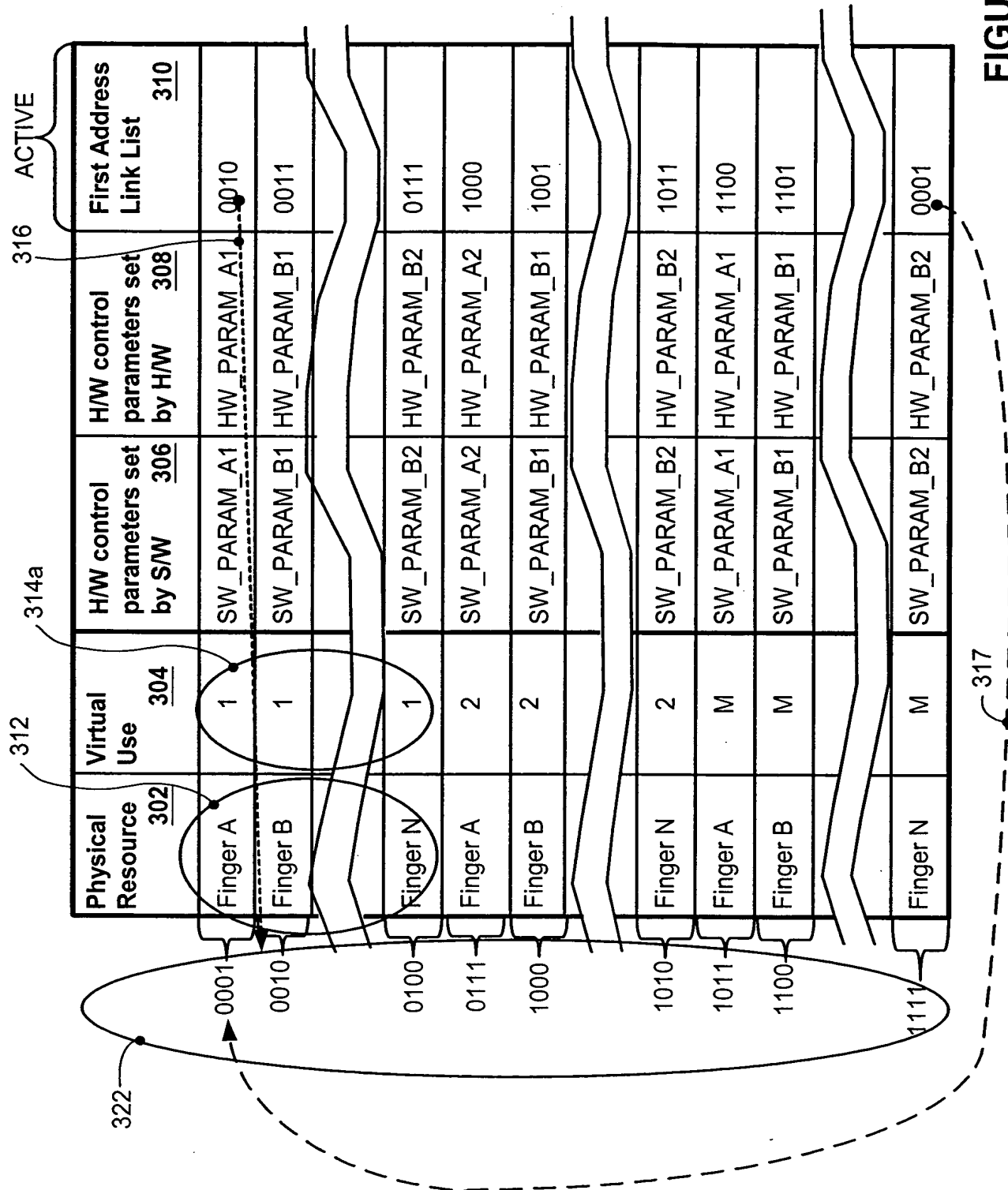


FIGURE 3A

300B

Physical Resource 302	Virtual Use 304	H/W control parameters set by S/W 306	H/W control parameters set by H/W 308	First Address Link List 310	Second Address Link List 320
Finger A*	1	SW_PARAM_A1	HW_PARAM_A1	0010	0010
Finger B	1	SW_PARAM_B1	HW_PARAM_B1	0011	0011
Finger N	1	SW_PARAM_B2	HW_PARAM_B2	0111	0111
Finger A	2	SW_PARAM_A2	HW_PARAM_A2	1000	1000
Finger B	2	SW_PARAM_B1	HW_PARAM_B1	1001	1010
Finger N	2	SW_PARAM_B2	HW_PARAM_B2	1011	1011
Finger A	M	SW_PARAM_A1	HW_PARAM_A1	1100	1100
Finger B	M	SW_PARAM_B1	HW_PARAM_B1	1101	0001
Finger N	M	SW_PARAM_B2	HW_PARAM_B2	0001	0001

FIGURE 3B

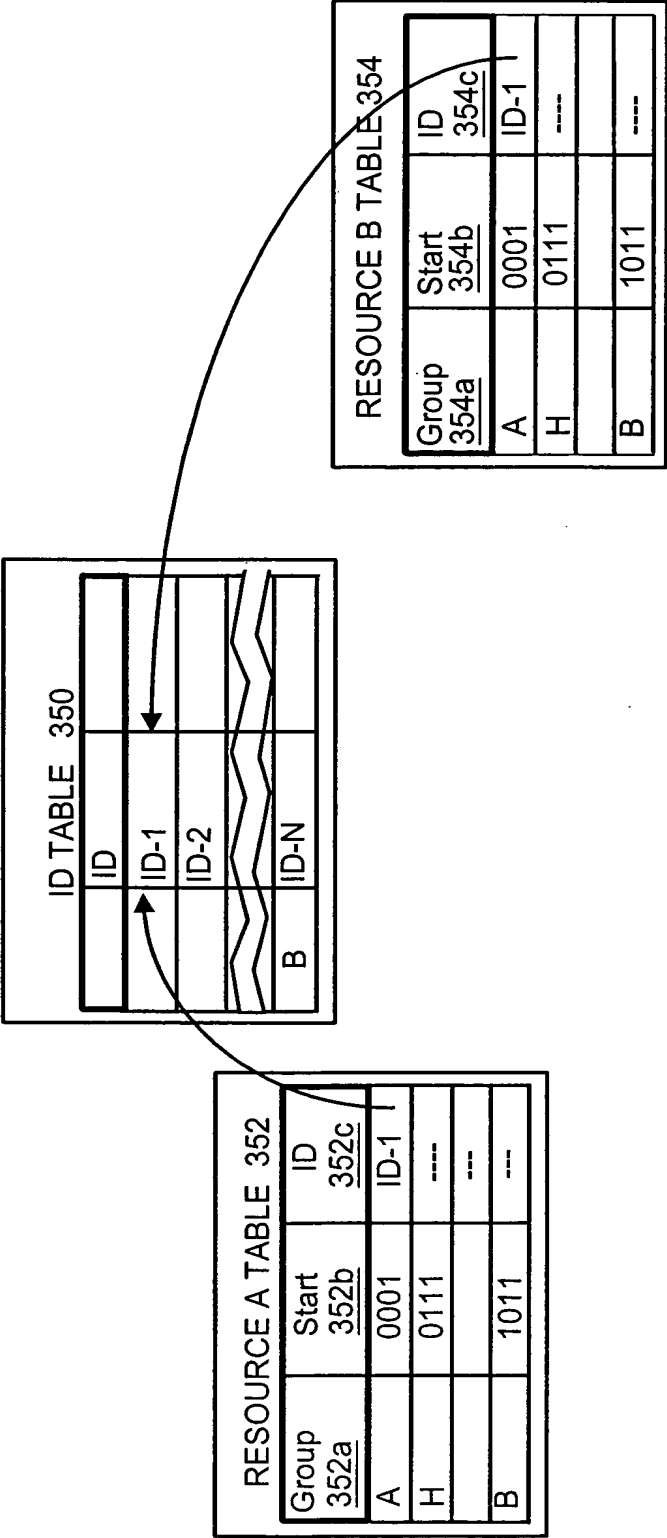


FIGURE 3C

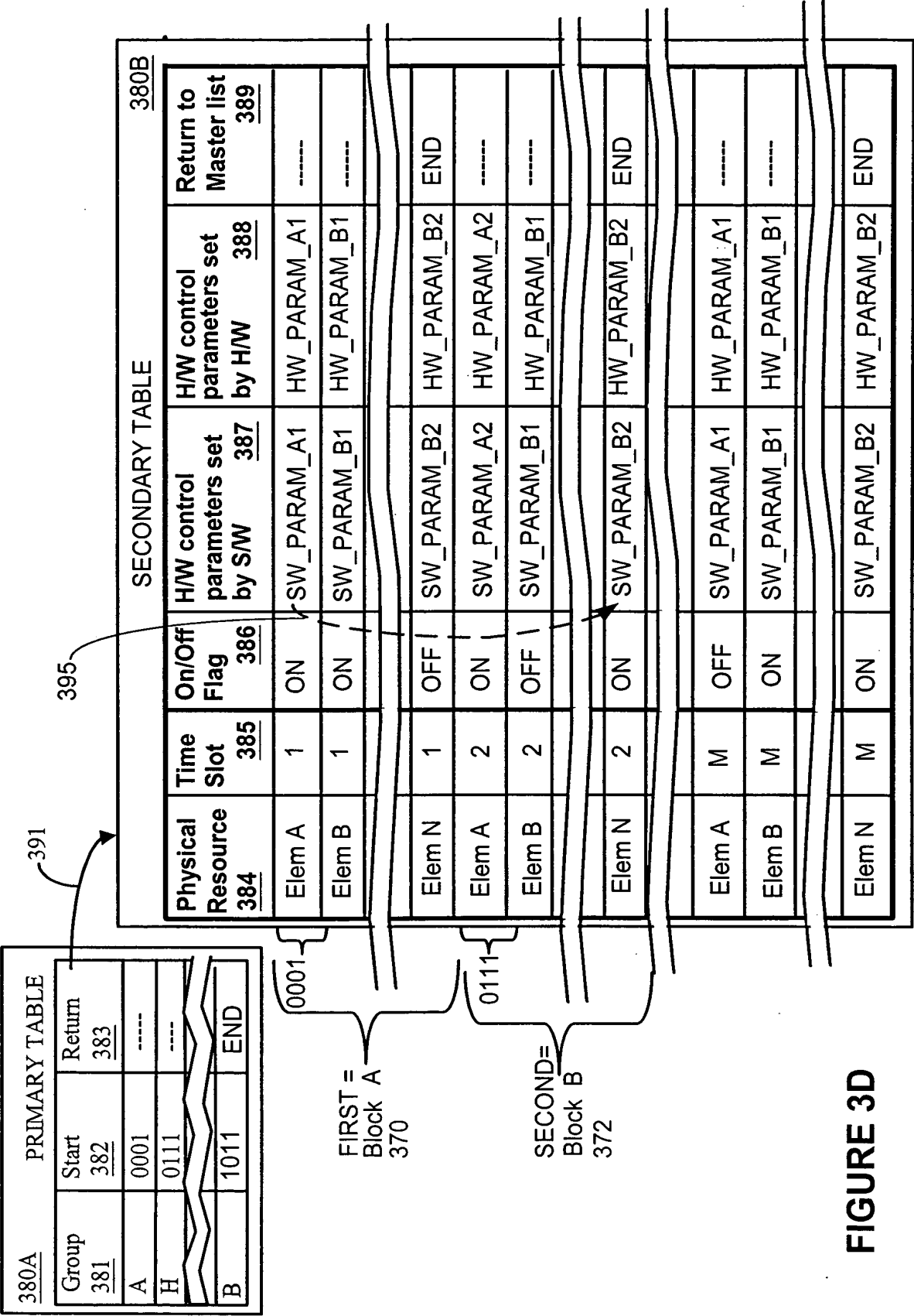


FIGURE 3D

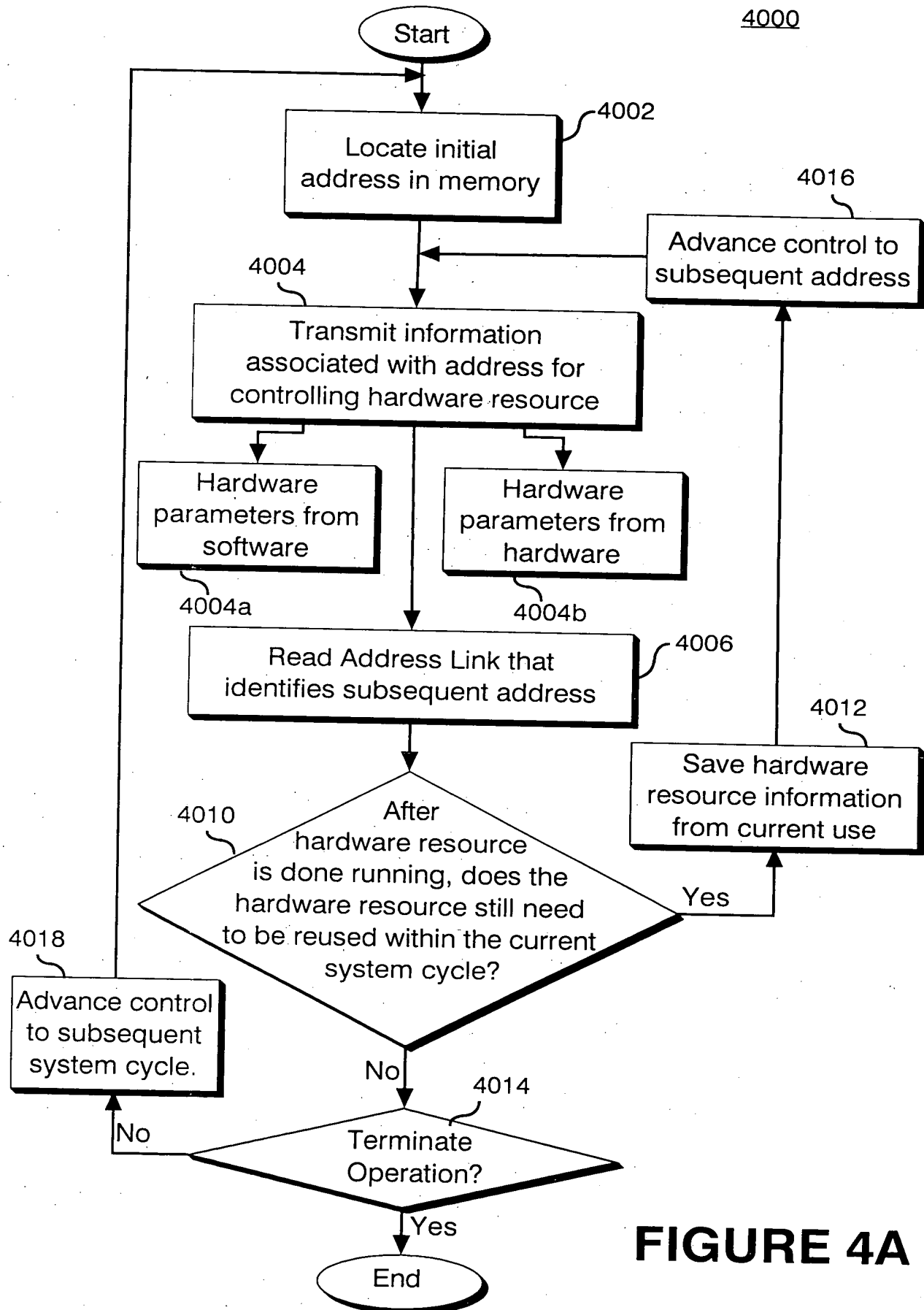


FIGURE 4A

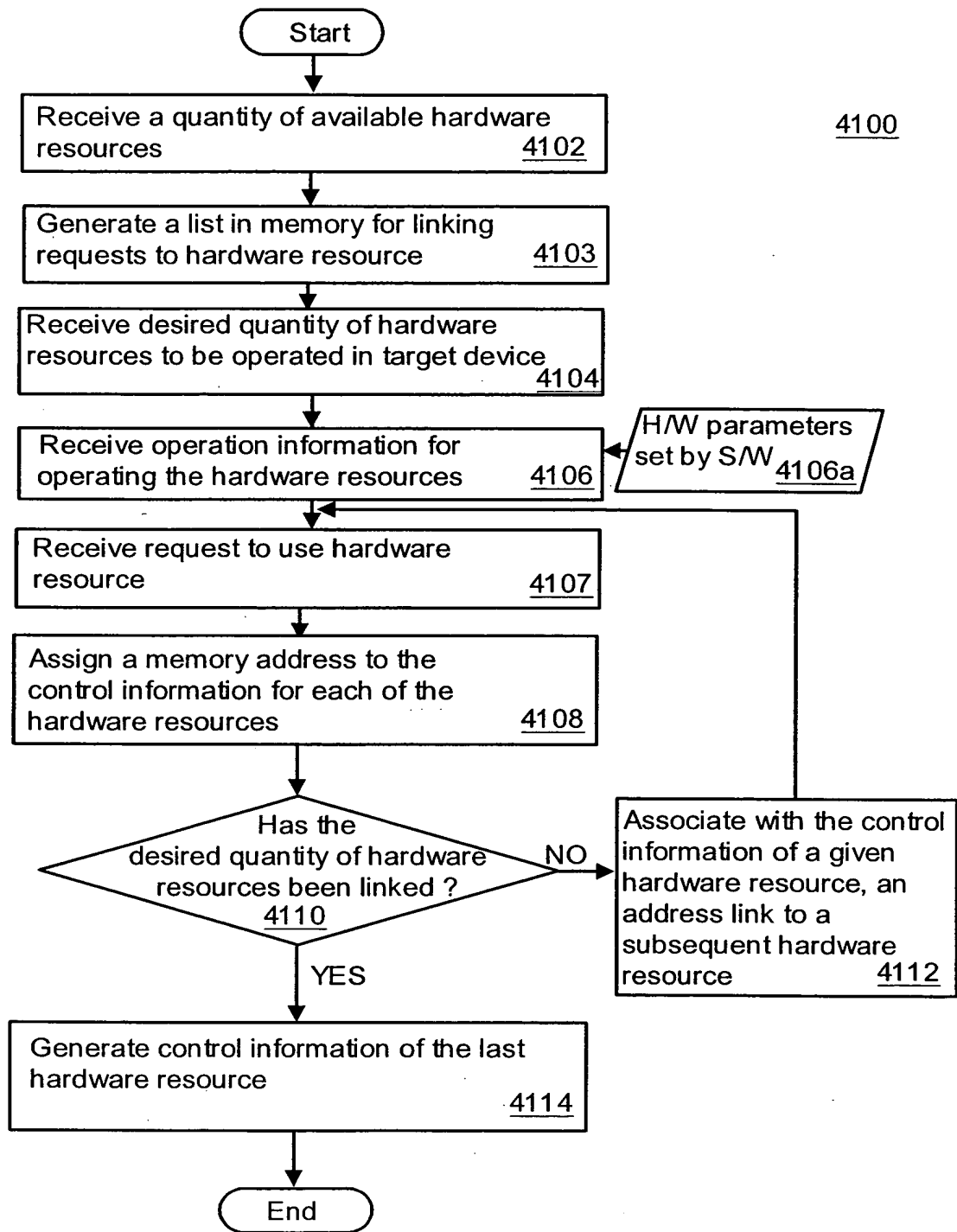


FIGURE 4B

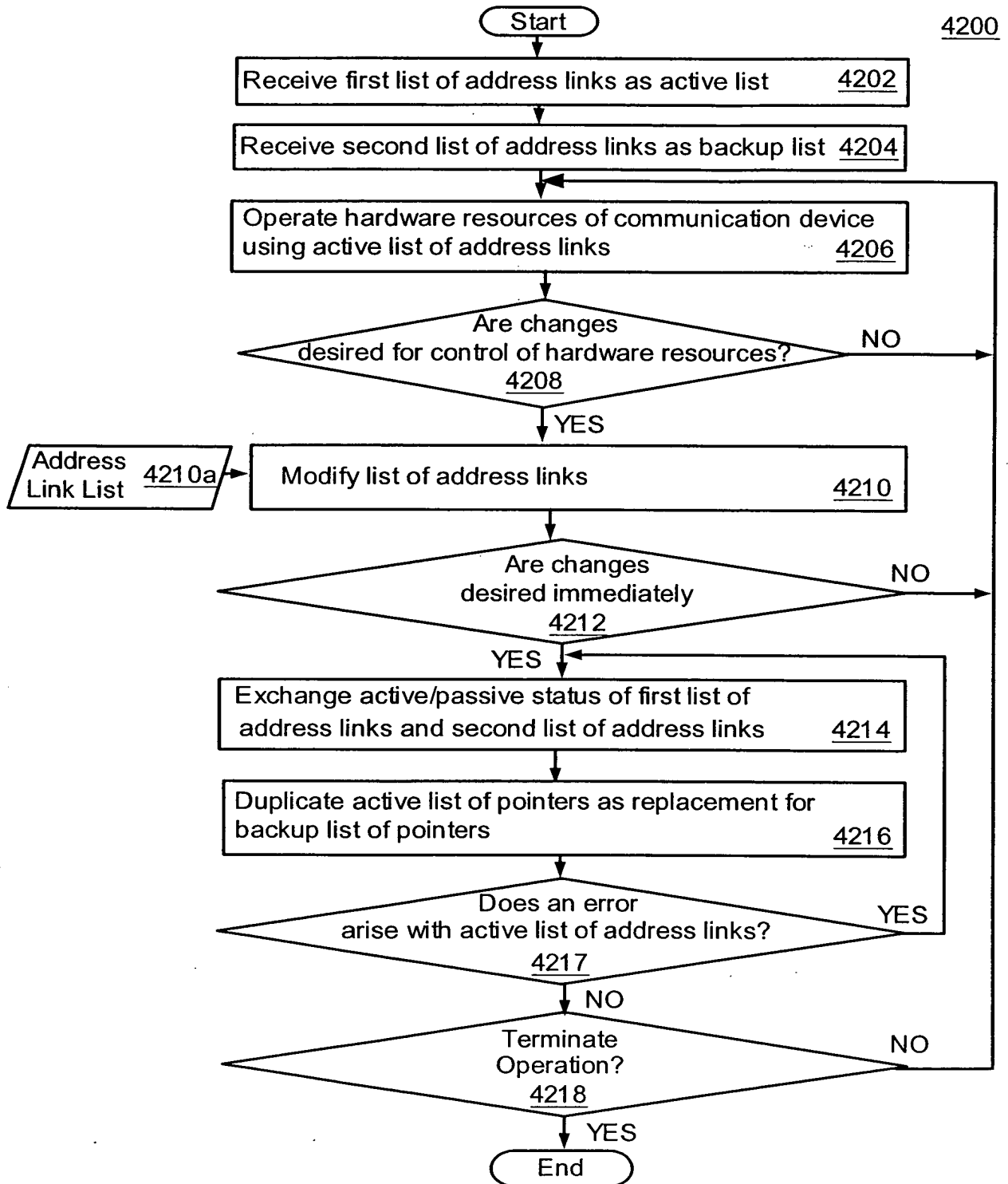


FIGURE 4C